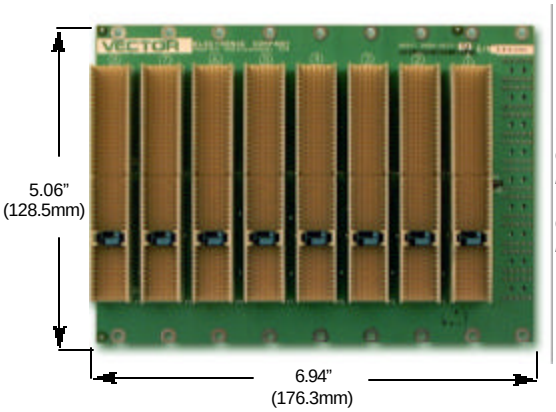
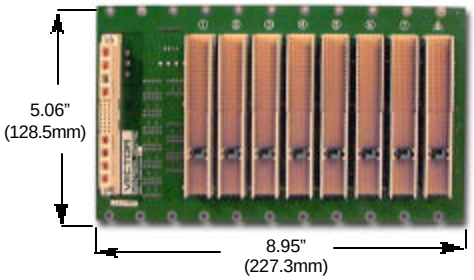
CompactPCI® Backplanes for 3U & 6U Cards
CPCIBP8-3



P/N
CPCIBP8-3-64
Right hand CPU

CPCIBP8-3L-32
Left hand CPU



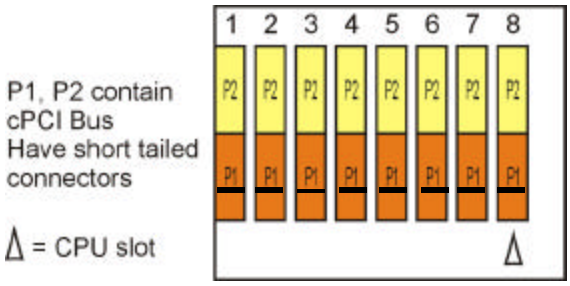
P/N CPCIBP8-3-1
with
Power Supply Connector

cPCI backplanes for standard applications are available in 8 slot configurations. Connectors used have pin spacing and conform to IEC 1076-4-101. The backplanes conform to PICMG specifications with either right or left hand control CPU slot.

cPCI performance with 32 or 64 bit operation at 33mHz allows 132mb/sec bandwidth. Rear I/O may be accommodated on a 32 bit model. Backplanes with power supply connectors are offered for use with Vector backplanes (see page 23).

Features:

- Compliant with PICMG 2.0 R2.1
- PICMG 2.1R1.0 Hot Swap Compliance
- V(I/O) Switchable from +5V to +3.3V
- Cadmium Yellow (+3.3V) and Brilliant Blue (+5V) keys supplied with each backplane
- Right or left side control slot on 3U versions
- Cable connection from power taps
- Backplane with integrated power supply connector



= if left hand CPU

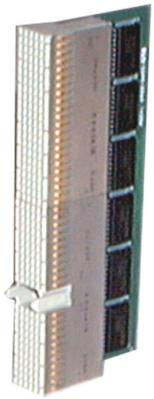
Part Number	Description	Connector Loading
CPCIBP8-3-32	8 slot, right control slot	Type A 8(25 row) P1 connectors Type B 1 (22 Row) P2 connectors, short tail
CPCIBP8-3L-32	8 slot, left control slot suitable for 32 bit & rear I/O connection	8 P1, with 8 long pin P2 (25 row) connectors; 1 (22row) with shrouds
CPCIBP8-3-64	8 slot, right control slot	P1 and P2 fully loaded (short tail pins)
CPCIBP8-3-1	8 slot, right control slot with M Din power supply connector	8 (25 row) P1 connectors, 1 (22 row) P2, short

Specifications

Physical	Material	FR4
	Flammability	UL94V-0
	Connectors Installed	UL94V-2
Environmental	Operating Temperature	0-70ºC
	Storage Temperature	-45 to 95ºC
	Humidity	0 to 95%, non-condensing
	Vibration	MIL 810E
Electrical	Connectors	Compliant with IEC 1076-4-101
	Power Input	3 Pin Mate-N-Lok
	Power Taps	20 Amps max, 40ºC
	Grounding	Chassis GND and Logic GND separate

Standards Compliance

PICMG 2.0 R2.2	CompactPCI Spec
PICMG 2.1 R1.0	Hot Swap Spec
IEEE 1101.10	Mechanical
IEEE 1101.11	Mechanical



TERMINATOR BOARD

Terminator board is used to reduce signal bounce and ringing in certain applications.Utilizes diode termination arrays. Board plugs J1 & J2 connectors into end slot of cPCI backplane in use.
Order separately:
P/N CPCITB-1

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